INCH-POUND MIL-PRF-19500/644 18 April 1997

PERFORMANCE SPECIFICATION

SEMICONDUCTOR DEVICE, DIODE, SILICON, POWER RECTIFIER, DUAL, COMMON CATHODE OR ANODE CENTER TAP, ULTRAFAST, TYPES 1N6768 THROUGH 1N6771 AND 1N6768R THROUGH 1N6771R JANTX, JANTXV, AND JANS

This specification is approved for use by all Departments and Agencies of the Department of Defense.

1. SCOPE

- 1.1 <u>Scope</u>. This specification covers the detail requirements for silicon, dual ultrafast, power rectifier diodes in a center-tap configuration. Three levels of product assurance are provided for each device type as specified in MIL-PRF-19500.
 - 1.2 Physical dimensions. See figure 1 (T0-257AA, isolated).
 - 1.3 Maximum ratings.

Types	$V_{RWM} 1/$ $I_D = 5 \mu A dc$	I _F <u>1/2</u> / T _C = +100°C	$I_{FSM} \underline{1}/$ $t_p = 8.3 ms$	R _θ Jc <u>1</u> /	R _{θJA}	T _{STG} and T _{OP}
	<u>V dc</u>	A dc	<u>A(pk)</u>	<u>°C/W</u>	°C/W	<u>°C</u>
1N6768, 1N6768R 1N6769, 1N6769R 1N6770, 1N6771R 1N6771, 1N6771R	50 100 150 200	8.0	60	3.8	45	-65 to +150

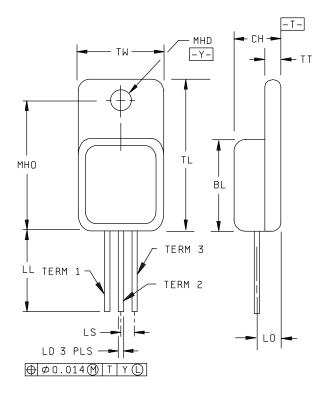
- 1/ Each individual diode.
- 2/ Derate at 160 mA/°C above T_c = +100°C.
- 1.4 Primary electrical characteristics. Unless otherwise specified, primary electrical characteristics are per diode at +25°C

Types	V_{F1} $I_F = 4 \text{ A dc}$	V _{F2} I _F =8 A dc	I_{R1} $V_{R} = 0.8 V_{RWM}$ (See 1.3)	I _{R2} V _R = 0.8 V _{RWM} (See 1.3)	t _{rr}	C _J V _R = 5 V f = 1 MHz
1N6768, 1N6768R 1N6769, 1N6769R 1N6770, 1N6771R 1N6771, 1N6771R	<u>V dc</u> 0.97	<u>V dc</u> 1.06	<u>µА dc</u> 10	T _c = +100°C μ <u>A dc</u> 250	<u>ns</u> 35	<u>p</u> E 150

Beneficial comments (recommendations, additions, deletions) and any pertinent data which may be of use in improving this document should be addressed to: Commander, Defense Supply Center, Columbus, ATTN: DSCC-VA, 3990 East Broad Street, Columbus, Ohio 43216-5000, by using the Standardization Document Improvement Proposal (DD Form 1426) appearing at the end of this document or by letter.

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FSC 5961



Dimensions						
Symbol	Inc	hes	Millim	neters		
	Min	Max	Min	Max		
BL	0.410	0.430	10.4	10.9		
СН	0.190	0.200	4.82	5.10		
LD	0.025	0.035	0.64	0.89		
LL	0.500	0.750	12.70	19.05		
LO	0.12	0.120 typ		3.05 typ		
LS	0.10	0 bsc	2.54 bsc			
MHD	0.140	0.150	3.55	3.80		
МНО	0.527	0.537	13.4	13.6		
TL	0.645	0.665	16.4	16.9		
TT	0.035	0.045	0.90	1.15		
TW	0.410	0.420	10.4	10.7		

NOTES:

- 1. Dimensions are in inches.
- 2. Metric equivalents are given for general information only. See 3.3.
- 3. All terminals are isolated from case.

SCHEMATIC



FIGURE 1. Physical dimensions and configuration (T0-257AA).

2. APPLICABLE DOCUMENTS

- 2.1 <u>General</u>. The documents listed in this section are specified in sections 3 and 4 of this specification. This section does not include documents cited in other sections of this specification or recommended for additional information or as examples. While every effort has been made to ensure the completeness of this list, document users are cautioned that they must meet all specified requirements documents cited in sections 3 and 4 of this specification, whether or not they are listed.
- 2.1.1 <u>Specifications, standards, and handbooks</u>. The following specifications, standards, and handbooks form a part of this document to the extent specified herein. Unless otherwise specified, the issues of these documents are those listed in the issue of the Department of Defense Index of Specifications and Standards (DODISS) and supplement thereto, cited in the solicitation (see 6.2).

SPECIFICATION

DEPARTMENT OF DEFENSE

MIL-PRF-19500 - Semiconductor Devices, General Specification for.

STANDARD

MILITARY

MIL-STD-750 - Test Methods for Semiconductor Devices.

(Unless otherwise indicated, copies of federal and military specifications, standards, and handbooks are available from the Standardization Documents Order Desk, Building 4D, 700 Robbins Avenue, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this document and the references cited herein (except for related associated specifications or specification sheets), the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

- 3.1 <u>Associated detail specification</u>. The individual item requirements shall be in accordance with MIL-PRF-19500 and as specified herein.
- 3.2 <u>Abbreviations, symbols, and definitions</u>. Abbreviations, symbols, and definitions used herein shall be as specified in MIL-PRF-19500.
- $3.3\,$ <u>Design, construction, and physical dimensions</u>. The design, construction, and physical dimensions shall be as specified in MIL-PRF-19500 and on figure 1. Methods used for electrical isolation of the terminal feedthroughs shall employ materials that contain a minimum of 90 percent Al_2O_3 (ceramic). Examples of such construction techniques are metallized ceramic eyelets or ceramic walled packages. The preferred measurements used herein is the metric SI system. However, since this item was originally designed using inch-pound units of measurement, in the event of a conflict between the metric and inch-pound units, the inch-pound units shall take precedence.
- 3.3.1 <u>Lead material and finish</u>. Lead finish shall be solderable in accordance with MIL-PRF-19500, MIL-STD-750, and herein. Where a choice of lead finish or formation is desired, it shall be specified in the acquisition document (see 6.2). When lead formation is performed, as a minimum, the vendor shall perform 100 percent hermetic seal in accordance with screen 14 of table II of MIL-PRF-19500 and 100 percent DC testing in accordance with group A, subgroup 2 herein.
 - 3.3.2 Polarity. Polarity and terminal configuration shall be in accordance with figure 1 herein.
 - 3.4 Marking. Marking shall be in accordance with MIL-PRF-19500.

- 4. QUALITY ASSURANCE PROVISIONS
- 4.1 Sampling and inspection. Sampling and inspection shall be in accordance with MIL-PRF-19500, and as specified herein.
- 4.2 Qualification inspection. Qualification inspection shall be in accordance with MIL-PRF-19500, and table III herein.
- 4.3 <u>Screening (JANTX, JANTXV, and JANS levels)</u>. Screening shall be in accordance with table IV of MIL-PRF-19500, and as specified herein. The following measurements shall be made in accordance with table I herein. Devices that exceed the limits of table I herein shall not be acceptable.

Screen (see	Measurement					
table IV of_MIL- PRF-19500)	JANS level	JANTX and JANTXV levels				
3C <u>1</u> /	Thermal impedance (see 4.3.2)	Thermal impedance (see 4.3.2)				
9 and 10	Not applicable	Not applicable				
11	I_{R1} and V_{F2}	I_{R1} and V_{F2}				
12	See 4.3.1, t = 120 hours	See 4.3.1, t = 48 hours				
13	Subgroups 2 and 3 of table I herein; $V_{F2} \text{and } I_{R1}; \ \Delta I_{R1} \leq 100 \text{ percent of initial value or } \pm 2.5 \mu A, \text{ whichever is greater; } \Delta V_{F2} \leq \pm 100 \text{ mV}.$	Subgroup 2 of table I herein; V_{F2} and I_{R1} ; $\Delta I_{R1} \le 100$ percent of initial value or $\pm 2.5\mu A$, whichever is greater; $\Delta V_{F2} \le \pm 100$ mV.				

- 1/ Thermal impedance shall be performed any time before screen 13.
- 4.3.1 Power burn-in conditions. Power burn-in conditions are as follows:
 - MIL-STD-750 method 1038, test condition A. $T_C = +125$ °C; $V_R = 0.8$ of rated V_{RWM} (see 1.3).
- 4.3.2 Thermal impedance $(Z_{\theta,JX})$ measurements for screening. The $Z_{\theta,JX}$ measurements shall be performed in accordance with MIL-STD-750, method 3101. Test each die separately. The maximum limit (not to exceed the group A, subgroup 2 limit) and conditions for $Z_{\theta,JX}$ in screening (table IV of MIL-PRF-19500) shall be derived by each vendor by means of statistical process control. When the process has exhibited control and capability, the capability data shall be used to establish the fixed screening limit. In addition to screening, once a fixed limit has been established, monitor all future sealing lots using a random five piece sample from each lot to be plotted on the applicable X, R chart. If a lot exhibits an out of control condition, the entire lot shall be removed from the line and held for Engineering evaluation and disposition.
- 4.3.2.1 Thermal impedance $(Z_{\theta,JX})$ measurements for initial qualification or requalification. The $Z_{\theta,JX}$ measurements shall be performed in accordance with MIL-STD-750, method 3101 (read and record data $Z_{\theta,JX}$). Derived conditions limits and thermal response curve shall be supplied to the qualifying activity on the qualification lot prior to qualification approval. Measurement conditions shall be in accordance with 4.4.1.
- 4.4 Quality conformance inspection. Quality conformance inspection shall be in accordance with MIL-PRF-19500 and as specified herein.

4.4.1 <u>Group A inspection</u>. Group A inspection shall be conducted in accordance with MIL-PRF-19500 and table I herein. Electrical measurements (end-points) and delta requirements shall be in accordance with the applicable steps of table II herein. The following test conditions shall be used for $Z_{\theta JX}$, group A inspection:

a.	I _M measure current	15 mA
b.	I _H forward heating current	10 A to 50 A
c.	t _H heating time	200 ms
d.	t _{MD} measurement delay time	35 μs
e.	V _H heating voltage	1 V

The maximum limit for $Z_{\theta JX}$ under these conditions are $Z_{\theta JX}(max) = 3.6^{\circ}C/W$

- 4.4.2 <u>Group B inspection</u>. Group B inspection shall be conducted in accordance with the conditions specified for subgroup testing in table VIa (JANS) and table VIb (JANTX and JANTXV) of MIL-PRF-19500 and as follows. Electrical measurements (end-points) and delta requirements shall be in accordance with the applicable steps of table II herein.
 - 4.4.2.1 Group B inspection, table VIa (JANS) of MIL-PRF-19500.

<u>Subgroup</u>	Method	Condition
4	1037	I_F or I_O = 1.25 A to 10 A; ΔT_J = +85°C minimum, for 2,000 cycles minimum.

4.4.2.2 Group B inspection, table VIb (JANTX and JANTXV) of MIL-PRF-19500.

Subgroup	<u>Method</u>	Condition
3	1037	I_F or I_O = 1.25 A to 10 A; ΔT_J = +85°C minimum, for 2,000 cycles minimum.

- 4.4.3 <u>Group C inspection</u>. Group C inspection shall be conducted in accordance with the conditions specified for subgroup testing in table VII of MIL-PRF-19500 and as follows. Electrical measurements (end-points) and delta requirements shall be in accordance with the applicable steps of table II herein.
 - 4.4.3.1 Group C inspection, table VII of MIL-PRF-19500.

<u>Subgroup</u>	<u>Method</u>	Condition
2	2036	Test condition A, 5 pounds, $t = 15$ seconds ± 3 seconds.
6	1037	I_F or I_O = 1.25 A to 10 A; ΔT_J = +85°C minimum, for 6,000 cycles minimum.

- 4.4.4 <u>Group E inspection</u>. Group E inspection shall be conducted in accordance with the conditions specified for subgroup testing in table III herein. Electrical measurements (end points) and delta requirements shall be in accordance with the applicable steps and footnotes of table II herein.
 - 4.5 Methods of inspection. Methods of inspection shall be as specified in the appropriate tables and as follows.
 - 4.5.1 Pulse measurements. Conditions for pulse measurement shall be as specified in section 4 of MIL-STD-750.

TABLE I. Group A inspection. 1/2/

Inspection		MIL-STD-750	Symbol	Li	mits	Unit
	Method	Conditions		Min	Max	
Subgroup 1						
Visual and mechanical examination	2071					
Subgroup 2						
Thermal impedance 3/	3101	See 4.4.1	$Z_{ heta JX}$		3.6	°C/W
Breakdown voltage 1N6768, 1N6768R 1N6769, 1N6769R 1N6770, 1N6770R 1N6771, 1N6771R	4022	$I_R = 5 \mu A dc$, pulsed $4/$	V_{BR}	50 100 150 200		V dc
Forward voltage	4011	$I_F = 4$ A dc, pulsed $\underline{4}/$ $I_F = 8$ A dc, pulsed $\underline{4}/$	$V_{F1} \ V_{F2}$		0.97 1.06	V dc V dc
Reverse leakage current	4016	DC method; pulsed $\underline{4}/$ V _R = 0.8 of rated V _{RWM} (see 1.3)	I _{R1}		10	μA dc
Subgroup 3						
High temperature operation:		T _C = +100°C				
Reverse leakage current	4016	DC method; pulsed $\underline{4}/$ $V_R = 0.8$ of rated V_{RWM} (see 1.3)	I _{R2}		250	μA dc
Low-temperature operation:		T _A = -55°C				
Forward voltage	4011	$I_F = 8 \text{ A dc, pulsed } \underline{4}/$	V_{F3}		1.17	V dc
Subgroup 4						
Scope display evaluation 5/	4023					
Reverse recovery time measurements	4031	Condition B; $I_F = 1.0$ A, di/dt = 50 A/ μ s	t _{rr}		35	ns
Subgroups 5 and 6						
Not applicable Subgroup 7						
Junction capacitance	4001	V _R = 5 V dc; f = 1.0 MHz	CJ		150	pF

 $[\]underline{1}$ / For sampling plan, see MIL-PRF-19500.

Each individual diode.

If 4.4.1 test conditions are performed in 100 percent screening, this test need not be performed in Group A.

Pulse test: pulse width = $300 \, \mu$ s, duty cycle ≤ 2 percent. The reverse breakdown characteristics shall be viewed on an oscilloscope with display calibration factors of 50 to $100 \, \mu$ A/division and 50 to $100 \, V$ /division. Reverse current over the knee shall be at least $500 \, \mu$ A. Each device may exhibit a slightly rounded characteristic and any discontinuity or dynamic instability of the trace shall be cause for rejection.

TABLE II. Groups A, B, C, and E electrical measurements. 1/2/3/

Step	Inspection	MIL-STD-750		Symbol	Limits		Unit
	·	Method	Conditions		Min	Max	
1	Forward voltage	4011	I _F = 8 A dc pulsed	V _{F2}		1.06	V dc
2	Reverse leakage current	4016	V_R = 0.8 of V_{RWM} (see 1.3) DC method, pulsed	I _{R1}		10	μA dc
3	Forward voltage	4011	I _F = 8 A dc pulsed	ΔV_{F2}		±100 mV initial read	
4	Reverse leakage current	4016	V_R = 0.8 of V_{RWM} (see 1.3) DC method, pulsed	Δl_{R1}		100 perce value or ±2.5μΑ c ever is gr	
5	Thermal impedance	3101	See 4.4.1	$Z_{ heta JX}$		3.6	°C/W

- 1/ The electrical measurements for table VIa (JANS) of MIL-PRF-19500 are as follows:
 - a. Subgroup 3, see table II herein, steps 1 and 2.
 - b. Subgroup 4, see table II herein, steps 1, 2, 3, 4, and 5.
 - c. Subgroup 5, see table II herein, steps 1, 2, 3, and 4.
- 2/ The electrical measurements for table VIb (JANTX and JANTXV) of MIL-PRF-19500 are as follows:
 - a. Subgroup 2, see table II herein, steps 1 and 2.
 - a. Subgroup 3, see table II herein, steps 1, 2, and 5.
 - a. Subgroup 6, see table II herein, steps 1 and 2.
- $\underline{3}$ / The electrical measurements for table VII of MIL-PRF-19500 are as follows:
 - a. Subgroups 2 and 3, see table II herein, steps 1 and 2 for all levels.
 - a. Subgroup 6, see table II herein, steps 1, 2, and 5 for all levels.

TABLE III. Group E inspection (all quality levels) for qualification only. 1/

Inspection		MIL-STD-750	Sampling
	Method	Conditions	plan
Subgroup 1 Thermal shock (temperature cycling)	1051	500 cycles	22 devices c = 0
Hermetic seal	1071		
Fine leak		Test condition H 5 x 10 ⁻⁷ atm cc/s	
Gross leak		Test condition C or K	
Electrical measurements		See table II herein, steps 1 and 2	
Subgroup 2			22 devices c = 0
Steady-state reverse bias	1038	Test condition A $t = 1,000$ hours, $T_C = +125^{\circ}C$ $V_R = 0.8$ of rated V_{RWM} (see 1.3)	C = 0
Electrical measurements		See table II herein, steps 1 and 2	
Subgroup 3			3 devices c = 0
Destructive physical analysis	2101		0 = 0
Subgroup 4			22 devices c = 0
Thermal resistance	3101	See 4.5.2; R _{0JC} = 3.8°C/W	0 - 0
Subgroup 5			15 devices c = 0
Barometric pressure (reduced)	1001	V _R = rated V _{RWM} (see 1.3)	0-0

^{1/} For initial design and process change verification only (one time testing).

4.5.2 <u>Thermal resistance</u>. Thermal resistance measurements shall be performed in accordance with method 3101 of MIL-STD-750. The maximum limits for R_{6UC} (max) shall be 3.8 °C/W for devices in the TO-257AA case style. Each diode leg shall be measured. The following parameter measurements shall apply:

- a. I_M measure current ----- 15 mA
- b. I_H forward heating current ----- 10 to 50 A
- c. t_H heating time ----- Steady state (see MIL-STD-750, method 3101)
- d. t_{MD} measurement delay time ----- 35 μs
- e. V_H heating voltage ----- 1 V
- 5. PACKAGING
- 5.1 Packaging requirements. The requirements for packaging must be in accordance with MIL-PRF-19500.
- 6. NOTES

(This section contains information of a general or explanatory nature that may be helpful, but is not mandatory.)

- 6.1 Notes. The notes specified in MIL-PRF-19500 are applicable to this specification.
- 6.2 Acquisition requirements. Acquisition documents should specify the following:
 - a. Title, number and date of the specification.
 - Issue of DODISS to be cited in the solicitation and, if required, the specific issue of documents referenced (see 2.2.1).
 - c. Lead finish or formation may be specified (see 3.3.1).
 - d. Type designation and product assurance level.
- 6.3 <u>Cross reference list</u>. Parts covered by this specification may be used to replace the following commercial Part of Identifying Numbers (PIN):

Preferred types	Commercial types
JANTX1N6768, JANTX1N6768R	1N6768, 1N6768R
JANTX1N6769, JANTX1N6769R	1N6769, 1N6769R
JANTX1N6770, JANTX1N6770R	1N6770, 1N6770R
JANTX1N6771, JANTX1N6771R	1N6771, 1N6771R
JANTXV1N6768, JANTXV1N6768R	1N6768, 1N6768R
JANTXV1N6769, JANTXV1N6769R	1N6769, 1N6769R
JANTXV1N6770, JANTXV1N6770R	1N6770, 1N6770R
JANTXV1N6771, JANTXV1N6771R	1N6771, 1N6771R

CONCLUDING MATERIAL

Custodians: Army - CR Navy - EC Air Force - 17 NASA - NA DLA - CC Preparing activity: DLA - CC

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I RECOMMEND A CHANGE:

1. DOCUMENT NUMBER
MIL-PRF-19500/644

2. DOCUMENT DATE
18 April 1997

3. DOCUMENT TITLE
SEMICONDUCTOR DEVICE, DIODE, SILICON, POWER RECTIFIER, DUAL, COMMON CATHODE OR ANODE CENTER TAP,

- ULTRAFAST, TYPES 1N6768 THROUGH 1N6771 AND 1N6768R THROUGH 1N6771R, JANTX, JANTXV, AND JANS

 4. NATURE OF CHANGE (Identify paragraph number and include proposed rewrite, if possible. Attach extra sheets as needed.)
- 5. REASON FOR RECOMMENDATION

6. SUBMITTER		
a. NAME (Last, First, Middle initial)	b. ORGANIZATION	
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